

## عنوان مقاله:

design a Cyclic 10Bit Analog To Digital Converters with A Ideal Switch With Removing Charge Injection

## محل انتشار:

شانزدهمین کنفرانس دانشجویی مهندسی برق ایران (سال: 1392)

تعداد صفحات اصل مقاله: 7

## نویسندگان:

Mina shirali - Islamic azad university bushehr

zoheir kord rostami - Islamic azad university bushehr

seyed abdolhdi mosavi - Islamic azad university bushehr

## خلاصه مقاله:

This paper is proposed A new 10-bit cyclic ADC .It isproposed switch and op-amp for enhancing the speed andremoving charge injection so reduce power. It is offered a newswitch for reduce leakage. In this paper is designed a cyclicADC with low power , high SFDR. The switched-capacitortechnique has been used in high-volume data conversion .It isdesigned an operational amplifier and switches to reducedpower .It is proposed an ideal op-amp. In this simulation is usedof process : 0.35um mixed mode (2p3m,3.3v/5v) poly cid andmodel , psim3 ( v3.1 ) . This simulation is used in level=49. Theproposed cyclic ADC is achieved a Spurious Free DynamicRange (SFDR) equal 66 dB . The power consumption of thecyclic ADC in 3.3 V supply is 11 mw , SNDR is 40db, ENOBequal to 10 , and THD .equal to -2. Finally values obtainedfrom this paper is compared with same sample and isacceptable

## کلمات کلیدی:

(Analog to digital converter cyclic, offset opamp,spurious free dynamic range (SFDR

## لینک ثابت مقاله در پایگاه سیویلیکا:

<https://civilica.com/doc/265176>

